

FIG. 1
(Prior Art)

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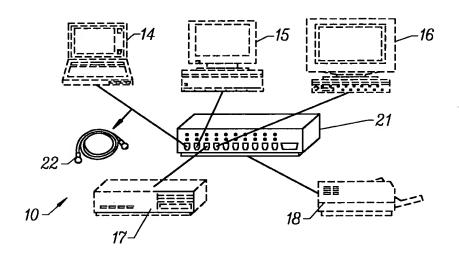


FIG. 2 (Prior Art)

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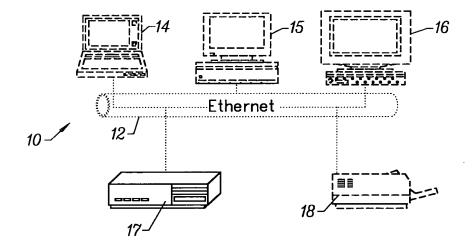


FIG. 3
(Prior Art)

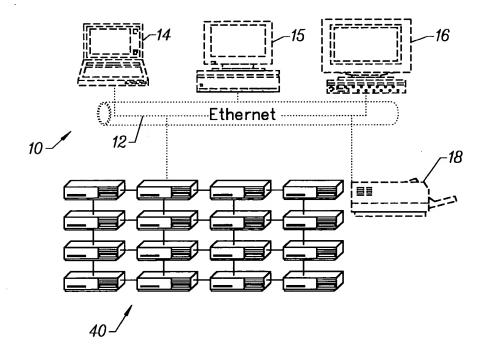


FIG. 4
(Prior Art)

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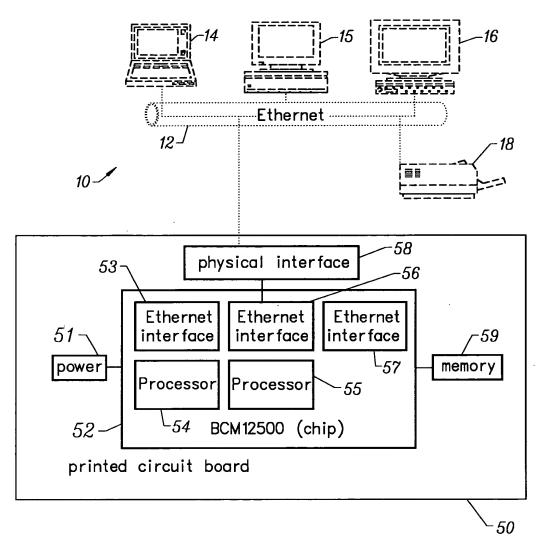


FIG. 5 (Prior Art)

7/7 Ctrl —To A CPU —CPUs RA ID CTRL CPUAL RAM PUAL CPU PUAL CPU RAM RAM DUAL CPU SPE PER PER RAM CPU CPU ME IN FTo A FCPUs "-DISK DRIVES "" RAM DUAL CPU CPU PVAL PUAL RA CPU CPU Ct. RAI CPU RAM DUAL CPU ₩. RAM —To A —CPUs RAM DUAL CPU **E** E E S SEA Ctrl RAM RAM CPU SE ENTRY CPU CPU CPU RAM RAM Ctrl —To A CPU —CPUs L 73-POWER SUPPLY "B" CPU Array S E E 劉慧 SPI PER CPU PUAL DUAL RAM CPU CPU RA Ξ Ctrl — To B CPU — CPUs RAID RAM CPU CPU RAM RAM RAM RA L SP PR PR RAM -N3 DUAL CPU RAM DUAL CPU SPU PAM CPU PAR RAM 4 RA 70 RAM PUAL CPU To B CPUs CP DE RA RA! RAM E E RAM 돌림 Seri RAM PE PE SPU PUR 돌림 RA RAM RAM RAM Ctrl —To B CPU — CPUs RAM DUAL N2-"A" CPU Array POWER SUPPLY SE EST PE PER PER CPU PURAL CPU SPU PER PER SPU PAR RAM RAM Z CPU1 CPU2 ** 92 **€** 훙 <u>₹</u> <u>S</u>

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